

Single Switch Flyback PFC Converter for Optimal Efficiency in BLDC Motor Drives

Gopalakrishnan V.¹, Ramachandiran A.²

¹Professor, ²PG Scholar, Department of Electrical and Electronics Engineering, Government College of Technology, Coimbatore, India

Email: ²ramaanadh12@gmail.com

Abstract

This research presents the design and implementation of a single-switch flyback PFC (Power Factor Correction) converter for Brushless DC (BLDC) motor applications. By operating in Discontinuous Conduction Mode (DCM), the converter achieves a power factor exceeding 0.9, improving efficiency and reducing energy losses. The converter provides electrical isolation, ensuring safety while integrating power conversion and power factor correction in a single stage. A lossless snubber circuit is included to mitigate voltage spikes and recycle leakage energy, further enhancing efficiency. Simulation results show the converter delivers 48V and 1A with high efficiency across varying loads. Its compact design reduces component count and manufacturing costs, making it ideal for space-efficient solutions in modern electrical systems. This work demonstrates the potential of single-switch flyback PFC converters in optimizing BLDC motor performance and ensuring reliable power quality.

Keywords: Single-switch flyback Converter, Power Factor Correction (PFC), Brushless DC (BLDC) Motor, Efficiency Enhancement.

1. Introduction

The single-switch flyback PFC (Power Factor Correction) converter is an innovative solution designed to enhance the efficiency and performance of Brushless DC (BLDC) motors, which are widely used in applications requiring high efficiency and reliability. As energy efficiency becomes increasingly critical across various industries, including electric vehicles,

renewable energy systems, and industrial automation, optimizing power conversion systems has become a priority. The flyback PFC converter provides an effective means to address these challenges while maintaining high power quality and reducing energy losses [1-3].

One of the key features of the single-switch flyback PFC converter is its ability to integrate power factor correction directly into the design. Power factor correction improves the power factor, which is essential for ensuring that electrical power is used effectively and reducing the overall energy consumption of a system. A low power factor leads to inefficiencies, increased energy costs, and excessive heat generation, all of which can impact the longevity and performance of electrical systems. By improving the power factor and ensuring compliance with modern energy standards, the converter offers a solution that helps meet the demands for higher efficiency in today's energy-conscious world[4,5].

The operation of the converter in Discontinuous Conduction Mode (DCM) further enhances its performance, particularly in scenarios where light loads are common. DCM operation ensures that the converter can perform efficiently even under varying load conditions, making it suitable for applications with fluctuating power requirements. This characteristic is particularly useful in real-world applications, where loads often shift between light and heavy demand [6-8].

To assess the converter's performance, MATLAB simulations are employed, enabling a detailed evaluation of its behavior under different load conditions. These simulations focus on aspects such as power factor, efficiency, and load response, offering insights into the converter's capabilities and optimization. The use of MATLAB allows for a comprehensive analysis of the converter's performance, ensuring that it meets the required standards for efficiency and reliability [9-12].

This study highlights the potential of the single-switch flyback PFC converter to significantly enhance the efficiency of BLDC motors. Its ability to improve energy conversion, reduce harmonic distortion, and provide electrical isolation makes it an ideal candidate for applications in ceiling fans, electric vehicles, and industrial automation systems [13,14]. By integrating power factor correction, improving efficiency, and reducing component count, this converter provides a compact, cost-effective, and reliable solution for optimizing motor performance and meeting modern energy standards. Through this work, the converter

demonstrates its potential to drive advancements in energy-efficient technologies across various industries.

2. Proposed System

This research presents a single-switch flyback PFC (Power Factor Correction) converter designed to improve the efficiency and performance of Brushless DC (BLDC) motors. Operating in Discontinuous Conduction Mode (DCM), the converter simplifies the circuit design while providing electrical isolation between the input and output, ensuring both safety and efficiency. It significantly enhances the power factor, meeting modern energy efficiency standards and reducing energy waste. A responsive control system is integrated to maintain a stable output voltage and optimize performance across various load conditions. The converter is ideal for applications in ceiling fans, electric vehicles, industrial automation, and renewable energy systems, offering broad applicability and the potential for substantial energy savings. Its compact and efficient design reduces the component count and lowers manufacturing costs, making it a cost-effective solution for diverse industries. The system is designed to achieve a unity power factor, improving both energy efficiency and system reliability. This work showcases the potential of the proposed converter to advance energy-efficient technologies, reduce environmental impact, and lower operational costs. Ultimately, this research offers a sustainable and efficient solution for motor-driven systems, paving the way for greater energy savings across various applications.

3. Block Diagram

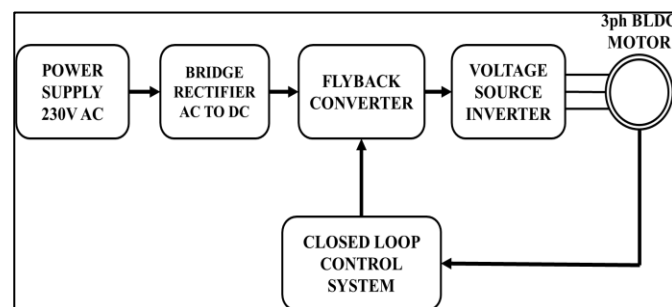


Figure 1. Block Diagram of BLDC Motor Drive System

Figure 1 shows the block diagram of the BLDC motor drive system. In this power supply setup, the 230V AC at 50Hz is connected to a bridge rectifier, which consists of an

uncontrolled bridge rectifier and a filter circuit to convert the AC voltage to DC. The filter minimizes ripple in the DC output, providing a smoother voltage for further processing. The converted DC is then processed by a flyback converter, which includes a MOSFET, high-frequency transformer, diode, and a DC link capacitor. This converter steps down the 230V DC input to a stable 48V DC output. The 48V DC is fed into a voltage source inverter, which converts it into 48V pulsating three-phase AC, suitable for motor drive applications. A PI controller is implemented in the closed-loop control block to improve the power factor by adjusting the flyback converter's voltage signal. The motor's performance is assessed with and without Power Factor Correction (PFC), considering key parameters like power factor, input power, output power, and efficiency. The evaluation involves varying the input AC voltage and observing the effects on overall system performance. This analysis helps determine the impact of PFC on motor performance, highlighting improvements in power factor and energy efficiency.

4. Circuit Diagram

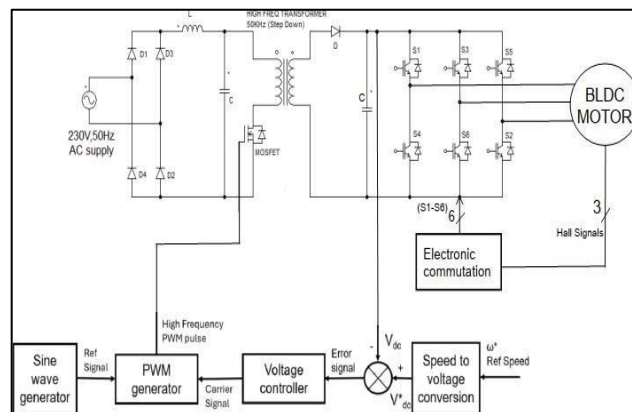


Figure 2. Circuit Diagram of BLDC Motor Drive System

Figure 2 illustrates the circuit diagram of BLDC Motor Drive System. The system begins with a 230V, 50Hz AC supply, which is fed into a diode bridge rectifier to convert the AC into pulsating DC voltage. This rectified DC is then smoothed by a filter to reduce ripples, providing a stable voltage for further processing. The stable DC voltage is directed to a flyback converter, where it is stepped down to a lower AC voltage using a high-frequency transformer and a MOSFET that controls energy transfer. The stepped AC voltage is rectified and filtered again to produce a stable 48V DC output, which is used to power a Brushless DC (BLDC)

motor. The DC voltage is fed into an inverter, which converts it into a three-phase AC signal suitable for driving the motor.

To enhance motor performance, a closed-loop control system utilizing Pulse Width Modulation (PWM) is implemented, which adjusts the duty cycle to control the motor's speed and torque precisely. The closed-loop system monitors the motor's operation and optimizes performance by adjusting the PWM signal, improving efficiency and reducing energy waste. This integrated approach ensures that the BLDC motor operates efficiently with precise control, offering enhanced performance and energy savings, making it ideal for applications requiring high precision and reliability.

5. Closed Loop Control System

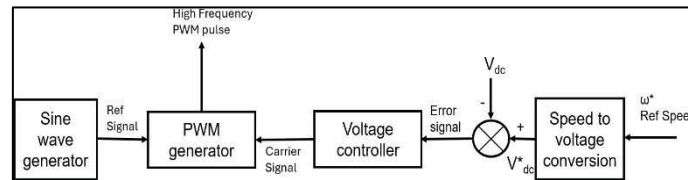


Figure 3. Block Diagram of Closed Loop Control System

Figure 3 depicts the block diagram of closed loop control system. This control system adjusts the power factor dynamically based on the rotor speed of a Brushless DC (BLDC) motor. The rotor speed is sensed using a Hall Effect sensor, which provides real-time feedback on the motor's rotational velocity. The sensor's output is processed through a MATLAB function to scale and filter the signal, preparing it for comparison with a reference voltage that represents the desired power factor. The processed signal is sent to a comparator, which generates an error signal when the flyback output voltage deviates from the reference. This error is fed into a PI controller, which minimizes the error by adjusting the control output. The PI controller's output is then used to generate a Pulse Width Modulation (PWM) signal, which controls the MOSFET in the flyback converter, regulating the output voltage. By varying the output voltage through PWM, the system improves the power factor, optimizing motor performance and energy efficiency under varying conditions.

6. Schematic Design and Formulations System Ratings

Input voltage = 230V

DC Link voltage (Vdc) = 48V Rated current = 1A

Rated Power = 30W Rated Speed = 350rpm

EMI Filter for Diode Bridge Rectifier Circuit

Filter capacitor value is determined using: $C_f = I_r / 2f * V_{pp}$

Filter inductor value is determined using: $L_f = 1 / 4\pi 2fc2C_f$

To design the Flyback Converter circuit

Duty Cycle: $V_o = 48V$

$V_s = 230V$ $F_s = 50KHz$

$$N_2/N_1 = V_o/V_s (1 - D)/D$$

Turns ratio is determined with $= N_2/N_1 = V_2/V_1$

The value of DC Link capacitor for flyback converter is determined using. $\Delta V_o/V_o = D/R * C * f_s$

Determine the load torque and motor voltage constant of BLDC motor:

$$P_o = 2\pi NTL/60$$

$$TL = P_o * 60/2\pi N Ke = V - 2IR_{ph}/Wm$$

7. Simulation Diagram

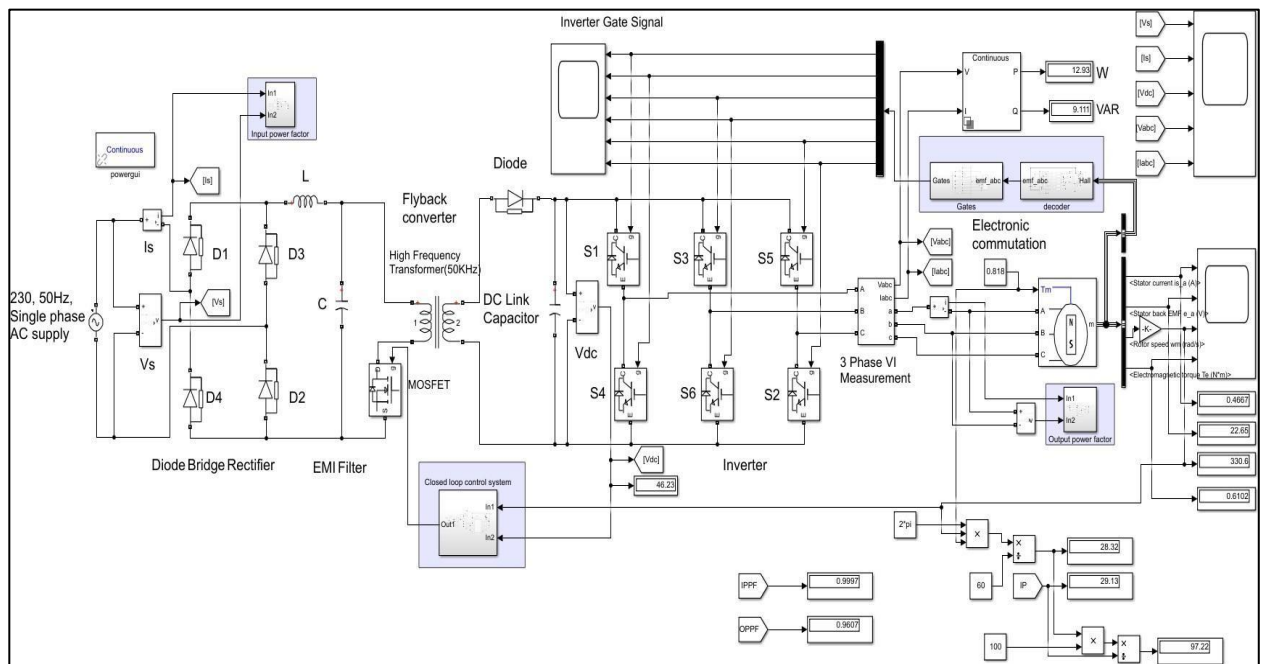


Figure 4. Simulation of BLDC Motor Drive System

Figure 4. presents the SIMULINK model diagram of BLDC motor drive system This Simulink diagram models a single-switch flyback PFC converter driving a BLDC motor. An AC input is rectified, filtered, and fed to the flyback converter, which regulates the DC link voltage. A closed-loop system controls the PFC stage for near-unity power factor. The inverter converts the DC link voltage to three-phase AC, driving the BLDC motor.

8. Simulation Results

The simulation results are given below for the following input Supply voltage

The supply voltage (V_s) for this circuit is 230V, and its waveform is shown below in Figure 5

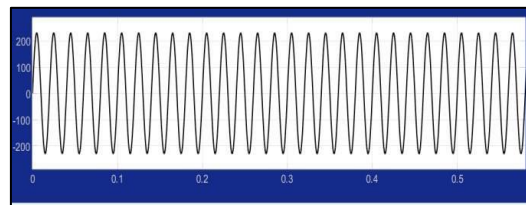


Figure 5. Waveform of Supply Voltage

Input Current

The Supply current (I_s) for this circuit is 0.13A , and its waveform is shown below in Figure 6.

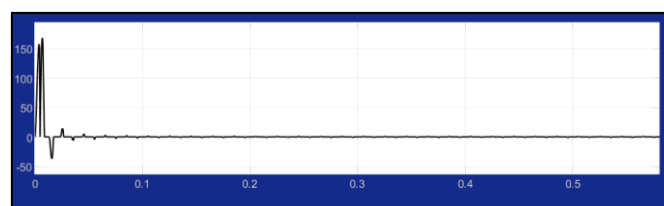


Figure 6. Waveform of Supply Current

Flyback Converter's output voltage waveform and simulated output value are shown below in Figure 7.

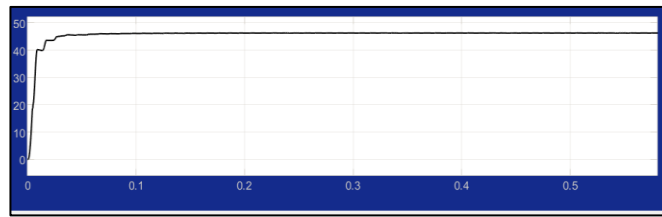


Figure 7. Output Waveform of Flyback Converter

The following waveforms in Figure 8 through 10 illustrates the three-phase output voltage, current of the inverter, and the output waveform of the BLDC Motor.

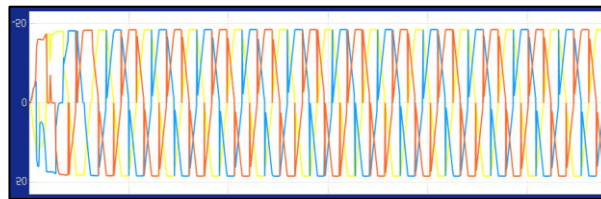


Figure 8. The Output Voltage Waveform of Three Phase VSI

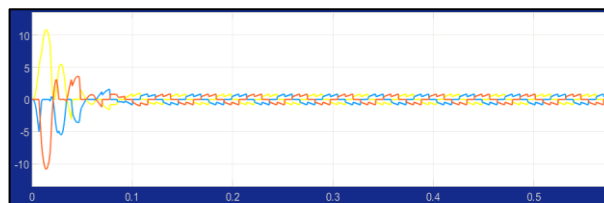


Figure 9. The Output Current Waveform of Three Phase VSI Output of BLDC Motor

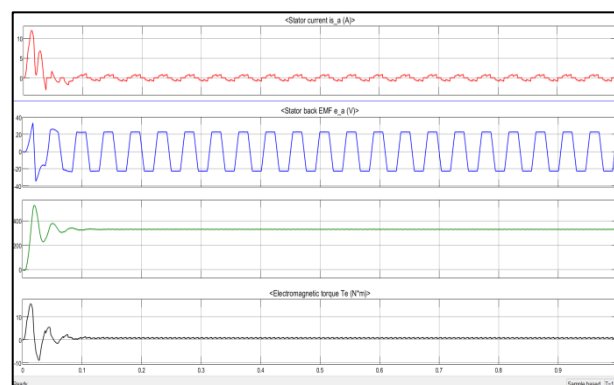


Figure 10. Output Waveform of BLDC Motor (Stator Current, Stator Back EMF, Motor Speed, and Electromagnetic Torque)

Table 1 and 2 depicts the results obtained for without and with power factor correction respectively.

Table 1. Without Power Factor Correction

S.No	Supply voltage (V)	DC link voltage (V)	Rotor Speed (rpm)	Input power (W)	Input power factor	Output power (W)	Output power factor	Efficiency (%)
1	120	23.5	165.9	15.26	0.9965	14.21	0.8227	93.12
2	150	29.7	212.7	19.26	0.9975	18.22	0.6796	94.62
3	180	35.93	256.3	24.32	0.9984	23.11	0.9608	95.02
4	210	42.15	300.3	27.19	0.999	25.72	0.9488	94.61
5	230	46.3	330.9	29.81	0.9993	28.34	0.9603	95.08

Table 2. With Power Factor Correction

S.No	Supply voltage (V)	DClink voltage (V)	Rotor Speed (rpm)	Input power (W)	Input power factor	Output power (W)	Output power factor	Efficiency (%)
1	120	23.45	166	14.94	0.9974	14.22	0.9909	95.18
2	150	29.67	211.5	18.85	0.9982	18.12	0.9429	96.14
3	180	35.82	255.8	22.7	0.9989	21.91	0.9711	96.53
4	210	42.09	299.9	26.52	0.9995	25.69	0.9552	96.88
5	230	46.23	330.6	29.13	0.9997	28.32	0.9607	97.22

As per the above observation, the proposed study achieved the power factor improvement for each step of input voltages with near to the value of unity power factor (such as 0.9909 at rated voltage of the system)

Input voltage Vs Output Power factor for without PFC and with PFC conditions are illustrated in Figure 11

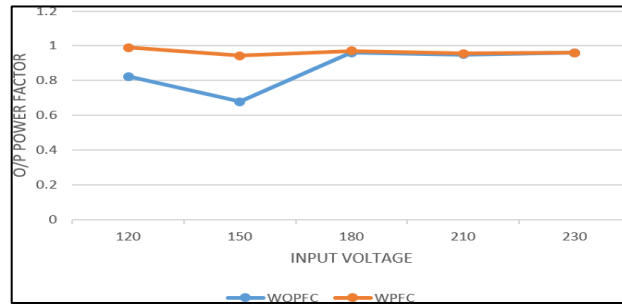


Figure 11. Characteristics of Input voltage Vs Output Power Factor for Without and With PFC

From the above characteristics, plot the relationship between input voltage and efficiency for each condition. Based on the observations, also improve the efficiency by improving the output power factor across input AC voltages from 120V to 230V. Figure 12 illustrates the efficiency observed for with and without PFC.

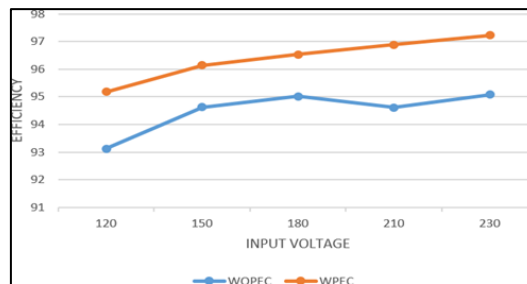


Figure 12. Comparison of Efficiency With and Without PFC

9. Conclusion

In research has successfully designed and simulated a Single Switch Flyback Power Factor Correction (PFC) Converter to optimize the efficiency of driving a Brushless DC (BLDC) motor using MATLAB. The flyback converter effectively converted voltage levels and enhanced the power factor, addressing challenges typically faced in conventional motor drive systems. Simulation results demonstrated significant improvements in both efficiency and power factor correction, validating the effectiveness of our design. By utilizing a closed-loop control system with Pulse Width Modulation (PWM), we achieved precise control over the output voltage, further enhancing the performance of the BLDC motor. The flexible topology of the flyback converter allows for seamless integration into various applications, making it a reliable solution for modern motor drives that require high efficiency. This research

highlights the potential of advanced converter designs in improving energy efficiency for electric motor applications, and future work could explore optimization techniques, such as incorporating adaptive control strategies and evaluating the converter's performance under varying load conditions, leading to further advancements in motor drive technology and more sustainable, efficient energy solutions.

References

- [1] Kumar, Amit, Utkarsh Sharma, and Bhim Singh. "Single switch boost-flyback PFC converter for BLDC motor drive used in ceiling fan." In 2020 IEEE International Conference on Computing, Power and Communication Technologies (GUCON), IEEE, 2020. 534-539
- [2] Kumar, Amit, and Bhim Singh. "Power factor corrected bridgeless converter for sensorless PMBLDC motor of ceiling fan." In 2020 IEEE First International Conference on Smart Technologies for Power, Energy and Control (STPEC), IEEE, 2020.1-6.
- [3] Abasian, Alireza, Hosein Farzanehfard, and Sayed Amir Hashemi. "A single-stage single-switch soft-switching (S6) boost-flyback PFC converter." IEEE Transactions on Power Electronics 34, no. 10 (2019): 9806-9813.
- [4] Gangavarapu, Sivanagaraju, Akshay Kumar Rathore, and Vinod Khadkikar. "High-efficiency three-phase single-stage isolated flyback-based PFC converter with a novel clamping circuit." IEEE Transactions on Industry Applications 56, no. 1 (2019): 718-729.
- [5] Singh, Bhim, and Vashist Bist. "A single sensor based PFC Zeta converter Fed BLDC motor drive for fan applications." In 2012 IEEE Fifth Power India Conference, IEEE, 2012. 1-6.
- [6] Lenin, Natesan Chokkalingam, Sanjeevikumar Padmanaban, Mahajan Sagar Bhaskar, Massimo Mitolo, and Eklas Hossain. "Ceiling fan drives—past, present and future." IEEE Access 9 (2021): 44888-44904.

- [7] Singh, Bhim, and Vashist Bist. "Power factor correction (PFC) converters feeding brushless DC motor drive." *International Journal of Engineering, Science and Technology* 7, no. 3 (2015): 65-75.
- [8] Manglik, Saurabh, Shubham Sundeep, and Bhim Singh. "Brushless DC motor based ceiling fan using buck-boost converter." In *2016 IEEE 7th Power India International Conference (PIICON)*, IEEE, 2016. 1-6.
- [9] Kumar, Amit, Utkarsh Sharma, and Bhim Singh. "PMBLDC motor based ceiling fan using an isolated PFC zeta converter." In *2020 IEEE International Conference on Power Electronics, Smart Grid and Renewable Energy (PESGRE2020)*, IEEE, 2020. 1-6.
- [10] Khan, Hashim Raza, Majida Kazmi, Haris Bin Ashraf, Muhammad Hashir Bin Khalid, Abul Hasan, and Saad Ahmed Qazi. "An isolated power factor corrected cuk converter with integrated magnetics for brushless dc ceiling fan applications." *Electronics* 10, no. 14 (2021): 1720.
- [11] Kumar, Amit, and Bhim Singh. "Sensorless BLDC Motor Ceiling Fan with an Isolated S 4 BL PFC Converter." In *2020 IEEE 5th International Conference on Computing Communication and Automation (ICCCA)*, IEEE, 2020. 83-88.
- [12] Kumar, Amit, Utkarsh Sharma, and Bhim Singh. "BLDC motor ceiling fan using a bridgeless isolated PFC SEPIC converter." In *2020 IEEE 9th Power India International Conference (PIICON)*, IEEE, 2020. 1-6.
- [13] Kumar, Amit, and Bhim Singh. "Power factor corrected bridgeless converter for sensorless PMBLDC motor of ceiling fan." In *2020 IEEE First International Conference on Smart Technologies for Power, Energy and Control (STPEC)*, IEEE, 2020. 1-6.
- [14] Bist, Vashist, and Bhim Singh. "A brushless DC motor drive with power factor correction using isolated zeta converter." *IEEE Transactions on Industrial Informatics* 10, no. 4 (2014): 2064-2072.